#### **Octave Equaliser**

This design could do with some modern enhancements, such as using NE5532 ic's as the op amps. I have a feeling that putting linear center tap pots instead of the ones shown in the circuit will improve the interaction between channels.

Oh, put the center tap to ground! This might improve the "woodles" on the graph at the bottom of the last page

$$R_2 = \frac{\Omega}{(2\Omega^2 - A_0) \,\omega_0 \,C_1} \tag{2.16.3}$$

For checking and trimming, use the following:

$$A_0 = \frac{R_3}{2R_1} \tag{2.16.4}$$

$$f_0 = \frac{1}{2\pi C_1} \sqrt{\frac{R_1 + R_2}{R_1 R_2 R_3}}$$
 (2.16.5)

$$\Omega = \frac{1}{2}\omega_0 R_3 C_1 \tag{2.16.6}$$

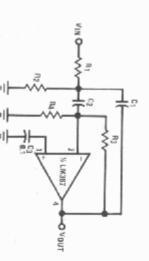


FIGURE 2.16.1 LM387 Bandpass Active Filter

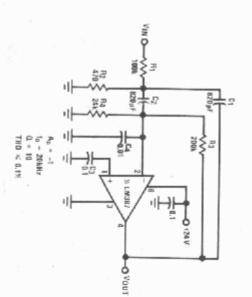


FIGURE 2.16.2 20kHz Bandpass Active Filter

### Example 2.16.1

Design a two-pole active bandpass filter with a center frequency  $t_0$  = 20kHz, midband gain  $A_0$  = 1, and a bandwidth of 2000Hz. A single supply,  $V_s$  = 24V, is to be used.

### Solution

1. 
$$Q \stackrel{\triangle}{=} \frac{f_0}{BW} = \frac{20 \text{kHz}}{2000 \text{Hz}} = 10, \quad \omega_0 = 2\pi t_0$$

Let R<sub>4</sub> = 24kΩ.

3. R<sub>3</sub> = 
$$\left(\frac{V_s}{2.6} - 1\right)$$
 R<sub>4</sub> =  $\left(\frac{24}{2.6} - 1\right)$  24k = 1.98 x 10<sup>5</sup>

Use R3 = 200k

From Equation (2.16.1):

$$R_1 = \frac{R_3}{2A_0} = \frac{200k}{2} = 100k$$

$$R_1 = 100k$$

5. Let C<sub>1</sub> = C<sub>2</sub>: then, from Equation (2.16.2):

$$C_1 = \frac{Q}{A_0 \omega_0 R_1} = \frac{10}{(1)(2\pi)(20k)(1 \times 10^5)} = 796 pF$$

From Equation (2.16.3):

$$R_2 = \frac{\Omega}{(2 \Omega^2 - A_0) \omega_0 C_1}$$

$$= \frac{10}{[(2)(10)^2 - 1](2\pi)(20k)(820pF)} = 488.$$
Use  $R_2 = 470\Omega$ 

The final design appears as Figure 2.16.2. Capacitor C<sub>3</sub> is used to AC ground the positive input and can be made equal to  $0.1\mu\mathrm{F}$  for all designs. Input shunting capacitor C<sub>4</sub> is included for stability since the design gain is less than 10.

## 2.17 OCTAVE EQUALIZERS

# 2.17.1 Ten Band Octave Equalizer

An octave equalizer offers the user several bands of tone control, separated an octave apart in frequency with independent adjustment of each band, it is designed to compensate for any unwanted amplitude-frequency or phase-frequency characteristics of an audio system.

A convenient ten band octave equalizer can be constructed based on the filter circuit shown in Figure 2.17.1 where the potentiomater R2 can control the degree of boost or cut at the resonant frequency set by the series filter of C2R<sub>S</sub> and L, by varying the relative proportions of negative feedback and input signal to the amplifier section.

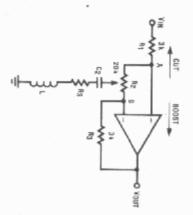


FIGURE 2.17.1 Typical Octave Equalizer Section

Assuming ideal elements, at the resonant frequency with R2 slider set to the mid position, the emplifier is at unity gain. With the slider of R2 moved such that C2 is connected to the junction of R1 and R2, the R<sub>S</sub>LC2 network will attenuate the input such that

$$\frac{V_{OUT}}{V_{IN}} = \frac{R_S}{3k + R_S}$$
 (2.17.1)

If the slider is set to the other extreme, the gain at the resonant frequency is:

$$\frac{\text{VOUT}}{\text{VIN}} = \frac{3k + R_S}{R_S} \tag{2.17.2}$$

In the final design,  $R_S$  is approximately  $500\Omega$ , giving a boost or attenuation factor of  $7~(\cong~17dB)$ . However, other filter sections of the equalizer connected between A and B will reduce this factor to about 12dB.

To avoid trying to obtain ten inductors ranging in value from 3.9H to 7.95mH for the ten octave from 32Hz to 16kHz, a simulated inductor design will be used. Consider the equivalent circuit of an inductor with associated series and parallel resistance as shown in Figure 2.12.2. The input impedance of the network is given by:

$$ZIN = \frac{SLRp}{(SL+Rp)} + Rs$$

$$= \frac{(Rp+Rs)\left(SL + \frac{RpRs}{Rp+Rs}\right)}{(SL+Rp)}$$

$$\therefore Z_{IN} = \frac{(R_p + R_s) \left(s + \frac{R_p R_s}{L(R_p + R_s)}\right)}{\left(s + R_p / L\right)}$$
(2.17.3)

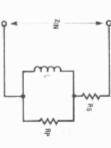


FIGURE 2.17.2 Ideal Inductor with Series and Parallel Resistances

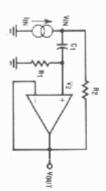


FIGURE 2.17.3 Simulated Inductor

This input impedance can be realised with the active circuit shown in Figure 2.17.3. Assuming an ideal amilfier with infinite gain and infinite input impedance,

$$V_2 = V_{OUT} = \frac{V_{INR_1}}{(I/sC_1 + R_1)}$$

(2.17.4)

ω

The input current I<sub>I</sub>N is given by.

$$l_{IN} = \frac{v_{IN} - v_2}{R_2} + \frac{v_{IN}}{(l_{IS}C_1 + R_1)}$$

(2.17.5)

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Substituting (2.17.4) into this expression gives:

$$I_{IN} = V_{IN} \left\{ \frac{1}{R_2} + \frac{sC_1}{(1 + sC_1R_1)} - \frac{R_1}{R_2(R_1 + \frac{1}{sC_1})} \right\}$$

$$= V_{IN} \left\{ \frac{1 + sC_1R_2}{R_2(sC_1R_1 + 1)} \right\}$$
Since  $Z_{IN} = \frac{V_{IN}}{I_{IN}}$ 

$$Z_{IN} = \frac{R_2 + sC_1R_1R_2}{1 + sC_1R_2}$$

$$\frac{R_1(\frac{1}{C_1R_2} + s)}{(\frac{1}{C_1R_2} + s)}$$
(2.17.6)

Equating (2.17.3) and (2.17.6)

$$\frac{(R_{p} + R_{s})}{s + \frac{R_{p}R_{s}}{L(R_{p} + R_{s})}} = \frac{R_{1}(s + \frac{C_{1}R_{1}}{c_{1}R_{2}})}{s + \frac{1}{C_{1}R_{2}}}$$

$$A_1 = B_D + B_S$$
 (2.17.7)

$$\frac{R_{p}R_{s}}{L(R_{p} + R_{s})} = \frac{1}{C_{1}R_{1}}$$

$$\therefore C_{1} = \frac{1}{R_{p}R_{s}}$$
(2.17.8)

$$\frac{R_p}{L} = \frac{1}{C_1 R_2}$$

$$\therefore R_2 = \frac{L}{R_p} \times \frac{R_p R_s}{L} = R_s$$
(2.17.9)

From the above equations it is apparent that R<sub>1</sub> should be large in order to, reduce the affect of R<sub>p</sub> on the filter operation, and to allow reasonably small capacitor values for each band (since capacitors will be non-polarized). R<sub>1</sub> should not be too large since it will carry the bias current for the non-inverting input of the amplifier.

The choice of Q for each of the filters depends on the permissible "ripple" in the boost or cut positions and the number of filters being used. For example, if we had only two filters separated by one octave, an ideal filter Q would be 1.414 so that the -3dB response frequencies will coincide, giving the same gain as that at the band centers. For the ten band equalizer a Q of 1.7 is better, since several filters will be affecting the gain at a given frequency. This will keep the maximum ripple at full boost or cut to less than ± 2dB.

## **EXAMPLE 2.17.1**

Design a variable ( $\pm$  12dB) octave equalizer section with a Q of 1.7 and a center frequency of 2kHz.

Solution

- Select R<sub>1</sub> = 68k
- 2. From equations (2.17.1) and (2.17.2)  $R_{\rm S} = 470$

$$L = \frac{\Omega R_8}{2\pi f_0} = \frac{\Omega R_2}{2\pi f_0}$$

$$\therefore L = \frac{1.7 \times 470}{2\pi \times 2 \times 10^3} = 63.6 \text{mH}$$
 (2.17.10)

4

$$C_1 = \frac{L}{R_D + R_S} = \frac{L}{\langle R_1 - R_2 \rangle R_2}$$

$$\frac{63.6 \times 10^{-3}}{\langle 68 \times 10^3 - 470 \rangle 470}$$

$$\therefore C_1 = 2000 \text{pF}$$

5. 
$$C_2 = \frac{1}{\omega_0^2 L}$$

$$= \frac{1}{(2\pi \times 2 \times 10^{3/2} 63.5 \times 10^{-3})}$$

$$\therefore C_2 = 0.1 \mu F$$
(2.17.11)

Table 2.17.1 summarizes the component values required for the other sections of the equalizer. The final design appears in Figure 2.17.4 and uses LM348 quad op-amps. Other unity gain stable amplifiers can be used. For example, LF356 will give lower distortion at the higher frequencies. Although linear taper potentiometers can be used, these will result in very rapid action near the full boost or full cut positions. S taper

fo(Hz)	C <sub>1</sub>	C2	R1	R <sub>2</sub>
32	0.12µF	4.7µF	75kΩ	5602
62	0.056 <sub>µ</sub> F	3.3µF	68k2	5102
125	0.033µF	1.5µF	62kΩ	5100
250	0.015µF	0.82µF	68kΩ	4709
500	8200pF	0.39µF	62kΩ	4709
7	3900pF	0.22µF	68kΩ	470 2
22	2000 pF	0.1µF	68kΩ	4702
4.K	1100pF	0.056µF	62kΩ	4702
8	510pF	0.022 µF	68kΩ	5102
16k	330pF	0.012µF	51kΩ	5102

potentiometers (Allen Bradley #70A1G032 R2035) will give a better response. All the capacitors used for tuning the simulated inductors (C2) should be non-polarized mylar or polystyrene.

Signal to noise ratio of the equalizer with the controls set "flat" is 73dB referred to a 1VRMS input signal. THD is under 0.01% at 20kHz.

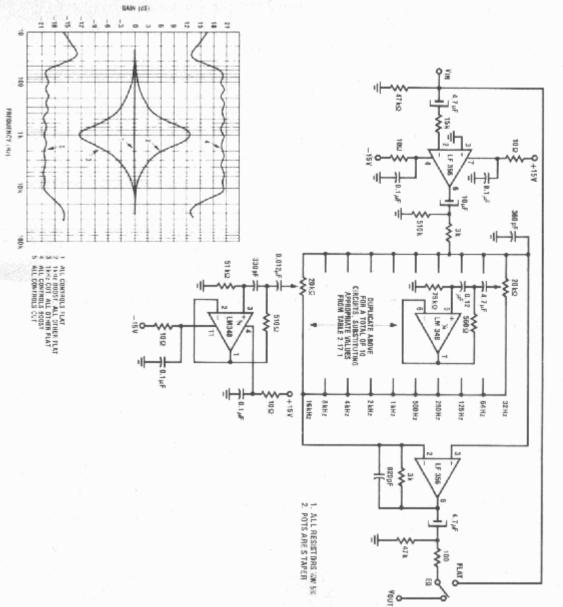


FIGURE 2.17.4 Complete Ten Band Octave Equalizer